

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
9	L10	47	("4211919"   "4417413"   "4463971"   "4501960"   "4539472"   "4572946"   "4575621"   "4605844"   "4611262"   "4634847"   "4683372"   "4707594"   "4714980"   "4719140"   "4720626"   "4727246"   "4733061"   "4755660"   "4758718"   "4763340"   "4766294"   "4777355"   "4795898"   "4797542"   "4797543"   "4798941"   "4806745"   "4812634"   "4818853"   "4827111"   "4835373"   "4845351"   "4851654"   "4853523"   "4868373"   "4916296"   "4942534"   "4960983"   "4962485"   "5068521"   "5113184"   "5175418"   "5182442"   "5196682"   "5202838"   "5220158"   "5399847").PN.	USP AT
10	L11	1	"4355300".PN.	USP AT
11	L12	9	4835373.URPN.	USP AT
12	L13	5	("3699311"   "3869082"   "4017834"   "4197524"   "4210900").PN.	USP AT
13	L14	5	("3699311"   "3869082"   "4017834"   "4197524"   "4210900").PN.	USP AT
14	L15	10	4280119.URPN.	USP AT

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
1	<b>L1</b>	<b>9</b>	("6073855" "5936841" "5786979" "5629838" "5532658" "5471040" "5451763" "4835373" "4280119").pn.	<b>USP</b> <b>AT;</b> <b>US-P</b> <b>GPU</b> <b>B</b>
2	<b>L2</b>	<b>9</b>	<b>1 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP</b> <b>AT;</b> <b>US-P</b> <b>GPU</b> <b>B</b>
3	<b>L3</b>	<b>5</b>	<b>1 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP</b> <b>AT;</b> <b>US-P</b> <b>GPU</b> <b>B</b>
4	<b>L4</b>	<b>9</b>	<b>2 3</b>	<b>USP</b> <b>AT;</b> <b>US-P</b> <b>GPU</b> <b>B</b>
5	<b>L5</b>	<b>9</b>	("4454414"   "5375037"   "5434395"   "5608607"   "5619396"   "5689654"   "5716221"   "5736727"   "5763862").PN.	<b>USP</b> <b>AT</b>
6	<b>L7</b>	<b>3</b>	("4342143"   "5384434"   "5590016").PN.	<b>USP</b> <b>AT</b>
7	<b>L8</b>	<b>4</b>	("4600907"   "4716389"   "4816789"   "5138436").PN.	<b>USP</b> <b>AT</b>
8	<b>L9</b>	<b>13</b>	("2688739"   "3091754"   "3146425"   "3404382"   "3582908"   "3699311"   "3719804"   "3869082"   "4017834"   "4280119"   "5159181"   "5218189"   "5371346").PN.	<b>USP</b> <b>AT</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
15	<b>L16</b>	<b>3119</b>	<b>(333/202,204,236,238,245,246,247).ccls.</b>	<b>USP AT; US-P GPU B</b>
16	<b>L17</b>	<b>601</b>	<b>16 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
17	<b>L18</b>	<b>754</b>	<b>16 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
18	<b>L19</b>	<b>1127</b>	<b>17 18</b>	<b>USP AT; US-P GPU B</b>
19	<b>L20</b>	<b>233</b>	<b>19 and (chip\$1 die)</b>	<b>USP AT; US-P GPU B</b>
20	<b>L21</b>	<b>232</b>	<b>20 not 4</b>	<b>USP AT; US-P GPU B</b>
21	<b>L22</b>	<b>894</b>	<b>19 not 20</b>	<b>USP AT; US-P GPU B</b>

	L #	Hits	Search Text	DBs
22	L23	9	22 and (non\$1contact contactless)	USP AT; US-P GPU B
23	L24	187	22 and (semiconductor\$1 (integrated adj circuit\$1) ic)	USP AT; US-P GPU B
24	L25	1030	(333/181,184,185).ccls.	USP AT; US-P GPU B
25	L26	147	25 and (capacitive\$2 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
26	L27	118	25 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))	USP AT; US-P GPU B
27	L28	221	26 27	USP AT; US-P GPU B
28	L29	211	28 not (4 20 23 24)	USP AT; US-P GPU B

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>29</b>	<b>L30</b>	<b>819</b>	<b>25 not 29</b>	<b>USP AT; US-P GPU B</b>
<b>30</b>	<b>L31</b>	<b>154</b>	<b>30 and (chip\$1 die)</b>	<b>USP AT; US-P GPU B</b>
<b>31</b>	<b>L32</b>	<b>117</b>	<b>30 and (semiconductor\$1 (integrated adj circuit\$1) ic)</b>	<b>USP AT; US-P GPU B</b>
<b>32</b>	<b>L33</b>	<b>215</b>	<b>31 32</b>	<b>USP AT; US-P GPU B</b>
<b>33</b>	<b>L34</b>	<b>205</b>	<b>33 not (4 20 23 24 29)</b>	<b>USP AT; US-P GPU B</b>
<b>34</b>	<b>L35</b>	<b>0</b>	<b>(257/664,728).pn.</b>	<b>USP AT; US-P GPU B</b>
<b>35</b>	<b>L36</b>	<b>631</b>	<b>(257/664,728).ccls.</b>	<b>USP AT; US-P GPU B</b>

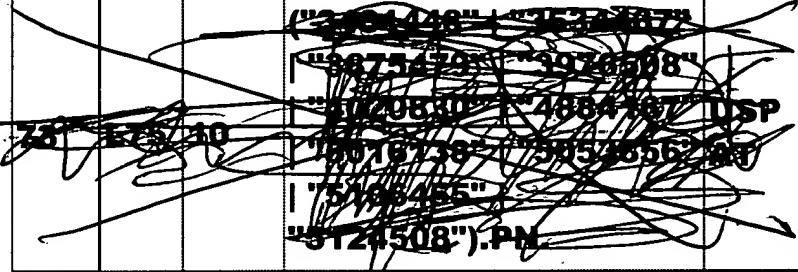
	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
36	<b>L37</b>	<b>56</b>	<b>36 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
37	<b>L38</b>	<b>60</b>	<b>36 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
38	<b>L39</b>	<b>103</b>	<b>37 38</b>	<b>USP AT; US-P GPU B</b>
39	<b>L40</b>	<b>66</b>	<b>39 not (4 20 23 24 29 34)</b>	<b>USP AT; US-P GPU B</b>
40	<b>L42</b>	<b>0</b>	<b>41 and (half adj capacitor\$1)</b>	<b>USP AT; US-P GPU B</b>
41	<b>L43</b>	<b>2</b>	<b>41 and (capacitor\$1 and isotropic)</b>	<b>USP AT; US-P GPU B</b>
42	<b>L44</b>	<b>1</b>	<b>us-6396712-.did.</b>	<b>DER WEN T</b>
43	<b>FAM ILY</b>	<b>1</b>	<b>1999-508689.NRAN.</b>	<b>DER WEN T</b>
44	<b>L46</b>	<b>0</b>	<b>6396712.URPN.</b>	<b>USP AT</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
45	<b>L41</b>	333	<b>vigushin\$.xa.</b>	<b>USP AT; US-P GPU B</b>
46	<b>L48</b>	415	<b>36 and (chip\$1 die)</b>	<b>USP AT; US-P GPU B</b>
47	<b>L49</b>	523	<b>36 and (semiconductor\$1 (integrated adj circuit\$1) ic)</b>	<b>USP AT; US-P GPU B</b>
48	<b>L50</b>	542	<b>48 49</b>	<b>USP AT; US-P GPU B</b>
49	<b>L51</b>	986	<b>(361/784,803,804).ccls.</b>	<b>USP AT; US-P GPU B</b>
50	<b>L52</b>	18	<b>51 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
51	<b>L53</b>	31	<b>51 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>52</b>	<b>L54</b>	<b>45</b>	<b>52 53</b>	<b>USP AT; US-P GPU B</b>
<b>53</b>	<b>L55</b>	<b>803</b>	<b>257/678.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>54</b>	<b>L56</b>	<b>17</b>	<b>55 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>55</b>	<b>L57</b>	<b>9</b>	<b>55 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>56</b>	<b>L58</b>	<b>24</b>	<b>56 57</b>	<b>USP AT; US-P GPU B</b>
<b>57</b>	<b>L59</b>	<b>55</b>	<b>257/924.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>58</b>	<b>L60</b>	<b>534</b>	<b>(439/68,69).ccls.</b>	<b>USP AT; US-P GPU B</b>

	L #	Hits	Search Text	DBs
59	L61	8	<b>60 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
60	L62	5	<b>60 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
61	L63	11	<b>61 62</b>	<b>USP AT; US-P GPU B</b>
62	L64	17	("3622384"   "3871014"   "3871015"   "4245273"   "4296272"   "4562513"   "4581680"   "4677254"   "4700473"   "4755631"   "4879156"   "4910643"   "4942079"   "5019997"   "5061825"   "5068596"   "5303116").PN.	<b>USP AT</b>
63	L65	2744	(361/760,761,762,763,7 64,767,768,770,771,782 ,783).ccIs.	<b>USP AT; US-P GPU B</b>
64	L66	85	<b>65 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>65</b>	<b>L67</b>	<b>65</b>	<b>65 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>66</b>	<b>L68</b>	<b>138</b>	<b>66 67</b>	<b>USP AT; US-P GPU B</b>
<b>67</b>	<b>L69</b>	<b>107</b>	<b>68 not (4 20 23 24 29 34 40 50 54 58 59 63)</b>	<b>USP AT; US-P GPU B</b>
<b>68</b>	<b>L70</b>	<b>2229</b>	<b>(174/260,261).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>69</b>	<b>L71</b>	<b>66</b>	<b>70 and (capacitive\$2 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>70</b>	<b>L72</b>	<b>83</b>	<b>70 and (electro\$1magnetic\$4 with (signal\$4 coupl\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>71</b>	<b>L73</b>	<b>132</b>	<b>71 72</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>72</b>	<b>L74</b>	<b>83</b>	<b>73 not (4 20 23 24 29 34 40 50 54 58 59 63 69)</b> 	<b>USP AT; US-P GPU B</b>